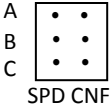
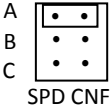
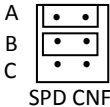
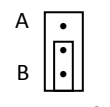
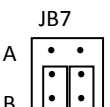
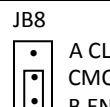
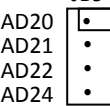

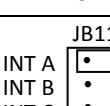
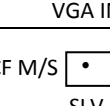


ESA-486AVR 486 Motherboard Standard Jumper Settings

Jumper Header	Jumper Description	Jumper Position - State	Jumper Diagram
JB2	SPD CNFG CPU Speed 100MHz	JB2_A OFF JB2_B OFF JB2_C OFF Default	 <p>JB2 A B C SPD CNFG</p>
JB2	SPD CNFG CPU Speed 66MHz	JB2_A ON JB2_B OFF JB2_C OFF	 <p>JB2 A B C SPD CNFG</p>
JB2	SPD CNFG CPU Speed 33MHz	JB2_A ON JB2_B ON JB2_C OFF	 <p>JB2 A B C SPD CNFG</p>
JB6	MINI PCI For DMA 1, move jumper to A-ON. Jumper must be moved in accordance with JB7 position A-ON & C-ON.	JB6_B (B-ON) Default	 <p>JB6 A B MINI PCI</p>
JB7	DRQ/GRT For DMA 1, use A-ON & C-ON. Jumpers must be moved in accordance with JB6 position A-ON.	JB7_B (B-ON) Default JB7_D (D-ON)	 <p>JB7 A B C D DRQ/GRT</p>
JB8	CMOS CLR/ENA	JB8_B (B-ON)	 <p>JB8 A CLR B ENA C CMOS</p>
JB9	PCI IDSEL	JB9_0 (AD20-ON)	 <p>JB9 AD20 AD21 AD22 AD24 PCI IDSEL</p>
JB10	VGA IDSEL	JB10_4 (AD24-ON) Pull jumper if not using on-board video.	 <p>JB10 AD20 AD21 AD22 AD24 VGA IDSEL</p>
JB11	VGA INTSEL On-board VGA interrupt setting.	JB11_A (INT A-ON)	 <p>JB11 INT A INT B INT C INT D VGA INTSEL</p>
HDR8	CF M/S Compact Flash Master/Slave setting	HDR8_Disabled	 <p>HDR8 CF M/S SLV MST</p>
ALL OTHER JUMPERS	-	OFF	