ESA-486AVR 486 Motherboard Standard Jumper Settings

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| **Jumper Header** | **Jumper Description** | **Jumper Position - State** | **Jumper Diagram** |
| JB2 | SPD CNFG  CPU Speed 100MHz | JB2\_A OFF JB2\_B OFF  JB2\_C OFF  Default | JB2  SPD CNFG  B  • •  • •  • •  A  C |
| JB2 | SPD CNFG  CPU Speed 66MHz | JB2\_A ON  JB2\_B OFF  JB2\_C OFF | JB2  SPD CNFG  B  • •  • •  • •  A  C |
| JB2 | SPD CNFG  CPU Speed 33MHz | JB2\_A ON  JB2\_B ON  JB2\_C OFF | JB2  SPD CNFG  B  • •  • •  • •  A  C |
| JB6 | MINI PCI  For DMA 1, move jumper to A-ON. Jumper must be moved in accordance with JB7 position A-ON & C-ON. | JB6\_B  (B-ON) Default | JB6  MINI PCI  •  •  •  B  A |
| JB7 | DRQ/GRT  For DMA 1, use A-ON & C-ON. Jumpers must be moved in accordance with JB6 position A-ON. | JB7\_B  (B-ON)  Default  JB7\_D  (D-ON) | JB7  DRQ/GRT  B  • •  • •  • •  A  C  D |
| JB8 | CMOS CLR/ENA | JB8\_B  (B-ON) | JB8  •  •  •  B ENA  A CLR    CMOS |
| JB9 | PCI IDSEL | JB9\_0  (AD20-ON) | • •  • •  • •  • •  JB9  PCI IDSEL  AD20  AD21  AD22  AD24 |
| JB10 | VGA IDSEL | JB10\_4  (AD24-ON)  Pull jumper if not using on-board video. | • •  • •  • •  • •  JB10  VGA IDSEL  AD20  AD21  AD22  AD24 |
| JB11 | VGA INTSEL  On-board VGA interrupt setting. | JB11\_A  (INT A-ON) | JB11  VGA INTSEL  INT A  INT B  INT C  INT D  • •  • •  • •  • • |
| HDR8 | CF M/S  Compact Flash Master/Slave setting | HDR8\_Disabled | CF M/S  HDR8  • • •      MST  SLV |
| ALL OTHER JUMPERS | - | OFF |  |